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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/834,660
 Priority Filing Date April 12, 2001
 Inventor Luan C. Tran
 Assignee Micron Technology, Inc.
 Priority Group Art Unit 2813
 Priority Examiner L. Schillinger
 Attorney's Docket No. MI22-1921
 Title: Semiconductor Processing Methods of Forming Transistors, Semiconductor
 Processing Methods of Forming Dynamic Random Access Memory Circuitry, and
 Related Integrated Circuitry

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

11046 U.S. PTO
 10/07/1453
 02/08/02

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending Application Serial No. 09/834,660, filed April 12, 2001, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). As a courtesy, Applicant submits a copy of the cited foreign reference only for review.

Citation of these references is respectfully requested.

Respectfully submitted,

Date:

Feb 16, 2002



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